REASONS FOR ALLOWANCE

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Thomas Presson on Feb 11, 2009.

The application has been amended as follows:

1) Regarding **independent claim 21** on page 12 of claims filed 1/8/2009, amend as follows:

A system comprising:

at least one processing device; and

one or more electronic memory devices that includes one or more computer-readable storage [medium] media for storing [having recorded thereon] a computer program that when executed on the at least one processing device [a processor] implements a reproduction method, the method comprising:

generating a recording clock frequency and a reproduction clock frequency;

storing electronic data on a first electronic storage device that utilizes the recording clock frequency to reproduce the electronic data;

storing the electronic data on a second electronic storage device that utilizes the reproduction clock frequency to reproduce the electronic data;

selecting either the recording clock frequency or the reproduction clock frequency as a function of user input;

converting the electronic data as a function of the selected clock frequency; and

outputting the converted electronic data,

wherein the recording clock frequency and the reproduction clock frequency are generated by dividing a fixed clock frequency according to a predetermined ratio,

wherein the predetermined ratio is set according the fixed clock frequency, the recording clock frequency, and the reproduction clock frequency, and

wherein the fixed clock frequency is maintained upon generating the recording clock frequency and the reproduction clock frequency so that a waiting period for clock stabilization is removed.

Therefore, the **amended claim 21** should read as follows:

A system comprising:

at least one processing device; and

one or more electronic memory devices that includes one or more computer-readable storage media for storing a computer program that when executed on the at least one processing device implements a reproduction method, the method comprising:

generating a recording clock frequency and a reproduction clock frequency;

storing electronic data on a first electronic storage device that utilizes the recording clock frequency to reproduce the electronic data;

storing the electronic data on a second electronic storage device that utilizes the reproduction clock frequency to reproduce the electronic data;

selecting either the recording clock frequency or the reproduction clock frequency as a function of user input;

converting the electronic data as a function of the selected clock frequency; and

outputting the converted electronic data,

wherein the recording clock frequency and the reproduction clock frequency are generated by dividing a fixed clock frequency according to a predetermined ratio,

wherein the predetermined ratio is set according the fixed clock frequency, the recording clock frequency, and the reproduction clock frequency, and

wherein the fixed clock frequency is maintained upon generating the recording clock frequency and the reproduction clock frequency so that a waiting period for clock stabilization is removed.

Allowable Subject Matter

2. **Claims 7-21** are allowed.

3. The following is an examiner's statement of reasons for allowance: **Independent claims 7, 14 and 21** recite the uniquely distinct features for "wherein the recording clock frequency and the reproduction clock frequency are generated by dividing a fixed clock frequency according to a predetermined ratio, wherein the predetermined ratio is set according the fixed clock frequency, the recording clock frequency, and the reproduction clock frequency, and wherein the fixed clock frequency is maintained upon generating the recording clock frequency and the reproduction clock frequency so that a waiting period for clock stabilization is removed"; **Independent claims 8 and 15** recite the uniquely distinct features for "wherein the first clock frequency and the second clock frequency are generated by dividing a fixed clock frequency according to a predetermined ratio, wherein the predetermined ratio is set according the fixed clock frequency, the first clock frequency, and the second clock frequency, and wherein the fixed clock frequency is maintained upon generating the first clock frequency and the second clock frequency so that a waiting period for clock stabilization is removed"; **Independent claims 9 and 11** recite the uniquely distinct features for "frequency dividing means for frequency-dividing a predetermined master clock for outputting a first clock frequency, and for frequency-dividing the predetermined master clock for outputting a second clock frequency, the second clock frequency being different from the first clock frequency" and "wherein the pre-determined master clock is maintained upon generating the first clock frequency and the second clock frequency so that a waiting period for clock stabilization is removed"; **Independent claims 16 and 18** recite the uniquely distinct features for "frequency-dividing a predetermined master clock for

outputting a first clock frequency, and for frequency-dividing the predetermined master clock for outputting a second clock frequency, the second clock frequency being different from the first clock frequency" and "wherein the pre-determined master clock is maintained upon generating the first clock frequency and the second clock frequency so that a waiting period for clock stabilization is removed"; The closest prior art of Fukami et al. (US 5,477,396), McNeely et al. (US 4,782,391), Fukushima et al. (US 6,477,204), Imai et al. (US 4,796,089), Ohmori (US 5,608,463), Hamadate (US 5,969,769), Patton et al. (US 6,307,597), Kim (US 6,556,252), teaches systems that divides a system clock to generate a second clock frequency for the purpose of picture in picture processing and does not teach that a period of clock stabilization is removed upon using the two generated frequencies and the predetermined master clock for the purpose of displaying two video signals at different clock frequencies, and therefore, the prior art either singularly or in combination, fail to anticipate or render the above underlined limitations obvious.

Conclusion

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to GELEK TOPGYAL whose telephone number is (571)272-8891. The examiner can normally be reached on 8:30am -5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thai Tran can be reached on 571-272-7382. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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